



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Raul-Adrian Cernea et al.

Title: Non-Volatile Memory and Method With Bit Line to Bit Line Coupled Compensation

Application No.: 10/667,222 Filing Date: September 17, 2003

Examiner: HOANG, Huan Group Art Unit: 2818

Docket No.: SNDK.308US0 Conf. No.: 7021

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

Please amend the application as follows:

Specification Amendments begin on page 2 of this paper.

Remarks begin on page 3 of this paper.

Attorney Docket No.: SNDK.308US0  
Express Mail No. EV437669103US

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